

WEST[Generate Collection](#)**Search Results - Record(s) 1 through 2 of 2 returned.**☐ 1. Document ID: US 6088786 A

L12: Entry 1 of 2

File: USPT

Jul 11, 2000

US-PAT-NO: 6088786

DOCUMENT-IDENTIFIER: US 6088786 A

TITLE: Method and system for coupling a stack based processor to register based functional unit

DATE-ISSUED: July 11, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Feierbach; Gary	Belmont	CA	N/A	N/A
Patel; Mukesh	Fremont	CA	N/A	N/A

US-CL-CURRENT: 712/200; 712/202, 712/203

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMIC	Draw Desc	Image
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☐ 2. Document ID: US 6032170 A

L12: Entry 2 of 2

File: USPT

Feb 29, 2000

US-PAT-NO: 6032170

DOCUMENT-IDENTIFIER: US 6032170 A

TITLE: Long instruction word controlling plural independent processor operations

DATE-ISSUED: February 29, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guttag; Karl M.	Missouri City	TX	N/A	N/A
Read; Christopher J.	Houston	TX	N/A	N/A
Balmer; Keith	Bedford	N/A	N/A	GBX

US-CL-CURRENT: 708/620; 708/518, 708/628

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMIC	Draw Desc	Image
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l11 and cpu	2

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